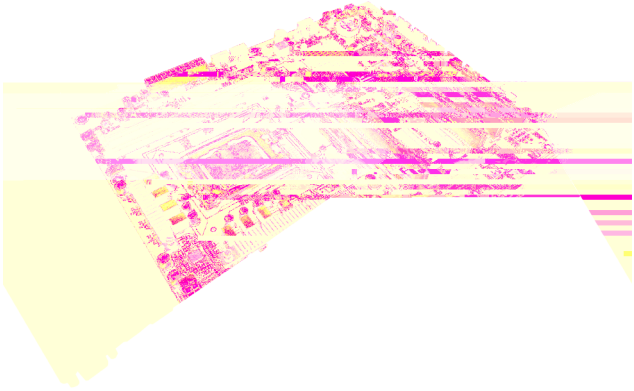
AIOT0-H610



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Hu Hu G

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6 WDT 33
6.1 33
6.1.1 33
6.1.2 35
7 GPIO 36
7.1 GPIO .....

1

1.1

J X 1
J X 1(1PCS/)
J SATA X 1
J I/O X 1



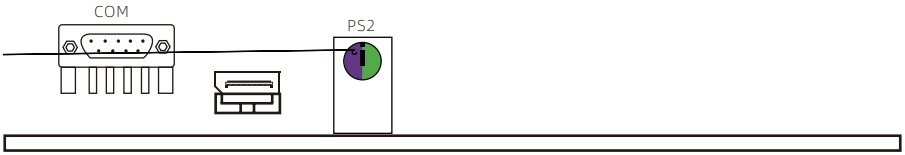
1.2

1.3

1.5

( )

1.6 IO



- COM:
- VGA: VGA
- DP: DP
- HDMI: HDMI
- PS2:
- USB2.0: USB2.0
- USB3.0: USB3.0
- LAN: RJ45
- AUDIO1:

2

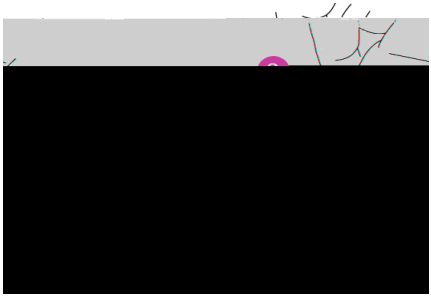
2.1 CPU

CPU

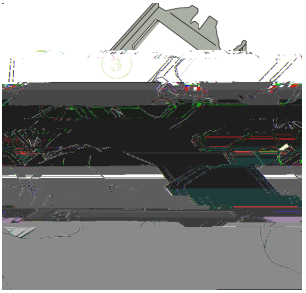
1. CPU G
2. CPU G
3.CPU CPU
G

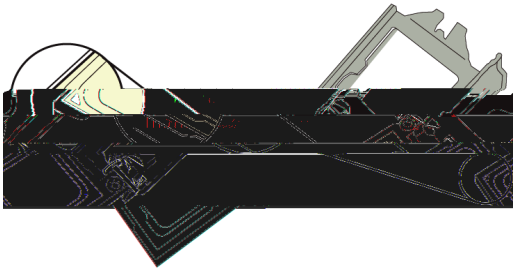
CPU

1. CPU CPU 90 G



2. CPU CPU G

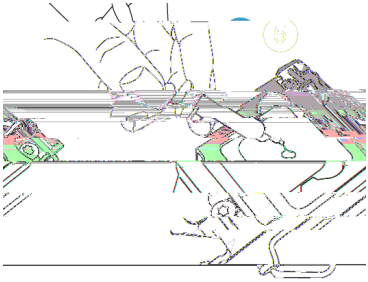




3. CPU CPU d d G



4. CPU G



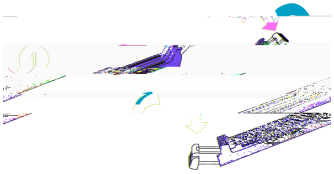
2.2

1. G
2. G
3. G

:

1. AC G
2. G
3.
4. 30 d d ,
   G ÿ ÿ
5. DIMM G

ÿ ÿ







⚠

G

3

3.1

2 Ÿ ÿ G
ÿ ÿ
G
3 1‘ 2 2‘ 3 Ÿ ÿ G



SHORT OPEN OPEN Pin1~2 SHORT Pin2~3 SHORT

1

1. "1" 1 G
2. G

3.2

JME1 Ÿ ME
ME 1-2 ÿ

1-2	Disable ME
2-3	NORMAL

CLR_COMS1 Ÿ 2-3
BIOS ÿ

1-2	NORMAL
2-3	CLEAR_COMS

ATX-M1 Ÿ1-2
2-3
ÿ

1-2	ATX Mode
2-3	AT Mode

JC9/JC10

1-2	USB
2-3	F_USB1 F_USB2 USB

COM1
JC1/JC2/JC3

RS232	RS485	RS422
JC1(1-2)	JC1(3-4)	JC1(5-6)
JC2(1-3)	JC2(3-5)	JC2(3-5)
JC2(2-4)	JC2(4-6)	JC2(4-6)
JC3(1-3)		JC3(3-5)
JC3(2-4)		JC3(4-6)

JCOM2
JC4/JC5/JC6

RS232	RS485	RS422
JC4(1-2)	JC4(3-4)	JC4(5-6)
JC5(1-3)	JC5(3-5)	JC5(3-5)
JC5(2-4)	JC5(4-6)	JC5(4-6)
JC6(1-3)		JC6(3-5)
JC6(2-4)		JC6(4-6)

COM1HJCOM2 RS-232/422/485 G

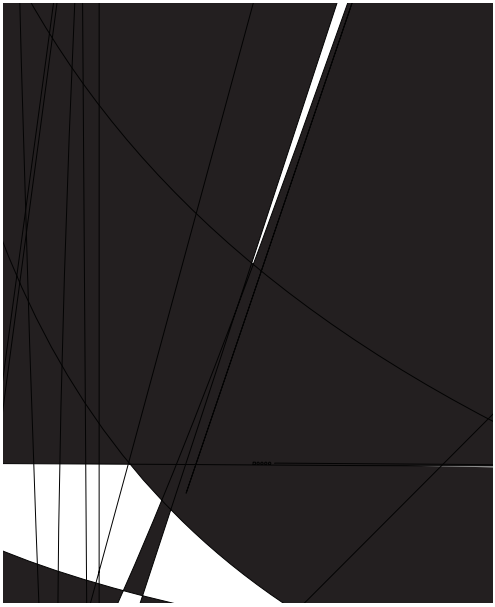
3.3 ATXPWR1 24Pin

3.7 J_GPIO1 GPIO

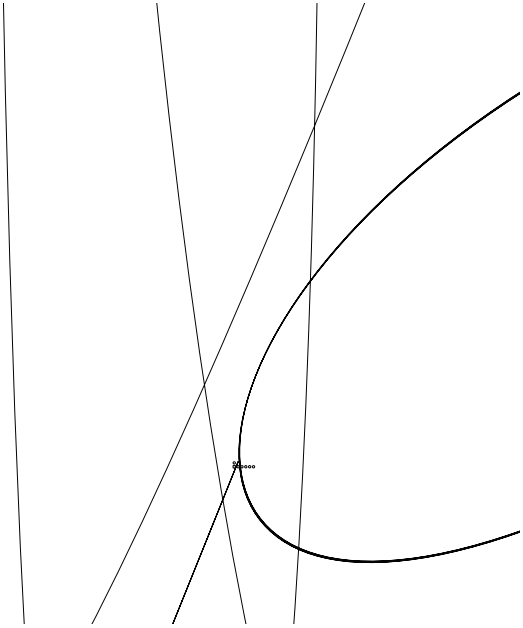
3.9 COM1/2

3.10 JCOM3/4/5/6 RS232

3.11 JESPI1



3.13 FPANEL1/2

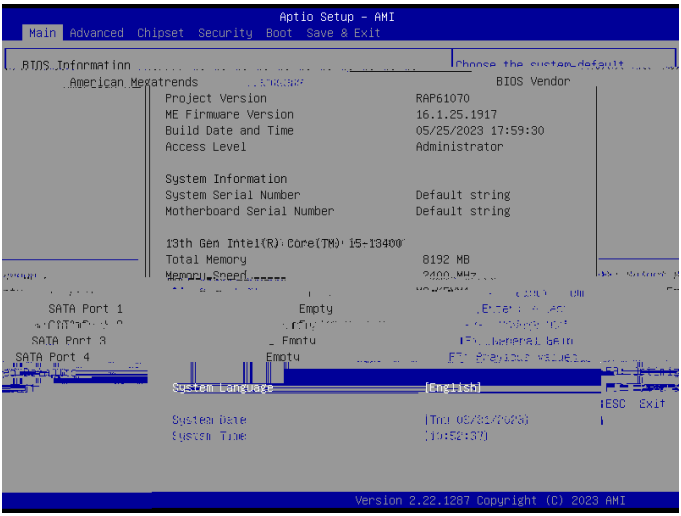


4 BIOS

4.1 BIOS



4.3 Main



BIOS Informawion(BIOS

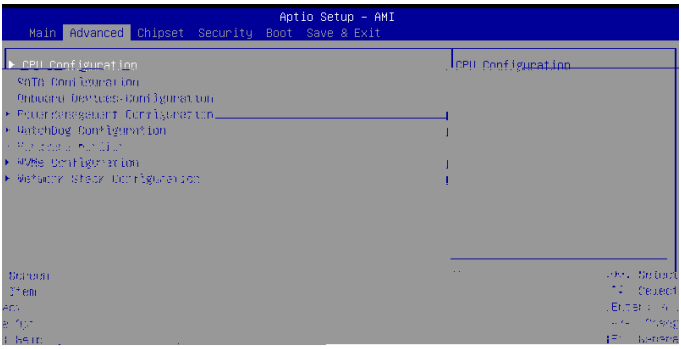
S|vwem Dawe ( )

, d / / d G

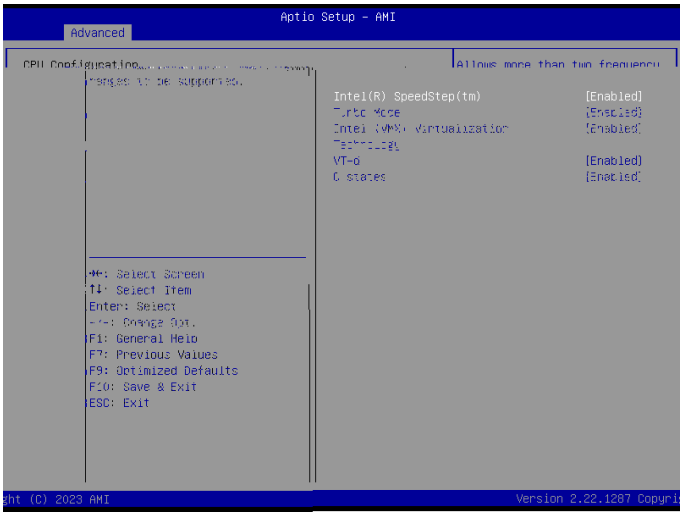
S|vwem Time

< >< >< > G

4.4 Adyanced



CPU Con gxrawion <Enwer>



Inwel(R) SpeedSwep(wm)

G

Enabled,Disabled.

Txrbo Mode

Turbo G

Enabled,Disabled.

Inwel (VMX) Virwxali}awion Technolog|

Intel MVX G

Enabled,Disabled.

VT-d

VT-d G

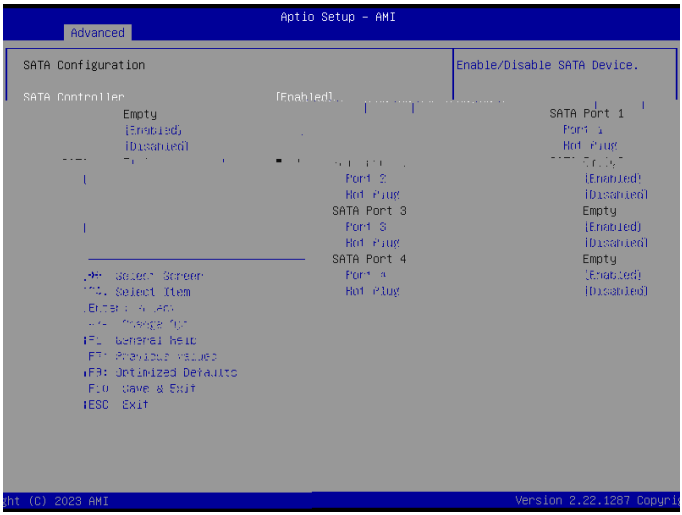
Enabled,Disabled.

C vwawev

CPU G

Enabled,Disabled.

SATA Con gxrawion <Enwer> Щ



SATA Conwroller

Intel MVX G

Enabled,Disabled.

Porw 1/2/3/4

G

Enabled,Disabled.

How Plxg

Enabled,Disabled.

<Esc> d Advanced d

Onboard Deyicev Con gxrawion <Enwer> Щ



IT8786 Sxper IO Con gxrawion

IT8786 IO G

Trxvwed Compxwing

G

PPT Con gxrawion

PPT G

Onboard Axdio

G

Enabled,Disabled.

PCH LAN Conwroller(I219)

PCH G

Enabled,Disabled.

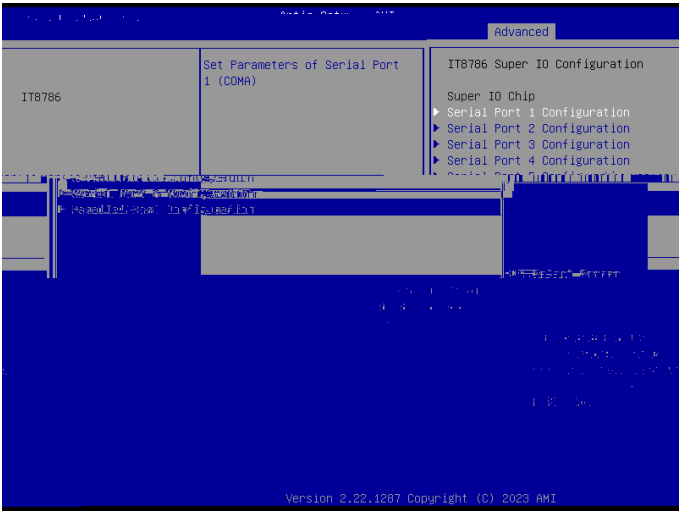
I226

G

Enabled,Disabled.

<Esc> d Advanced d

IT8786 Sxper IO Con gxrawion <Enwer>



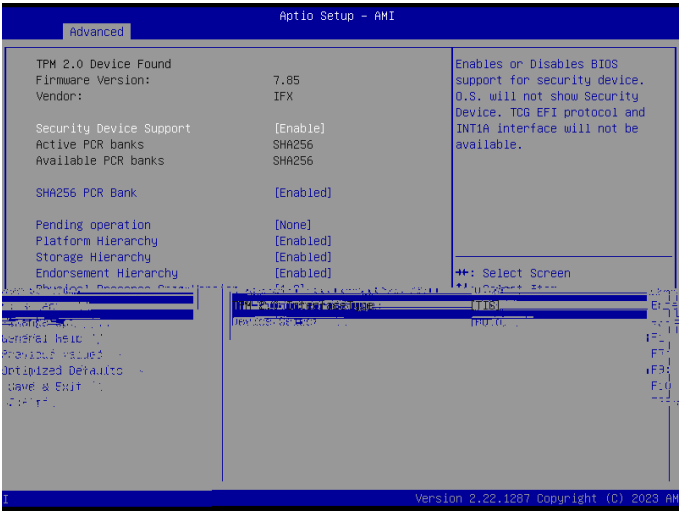
Serial Porw 1/2/3/4/5/6 Con gxrawion

G

Parallel Porw Con gxrawion

G

Trxvwed Compxwing Con gxrawion <Enwer>



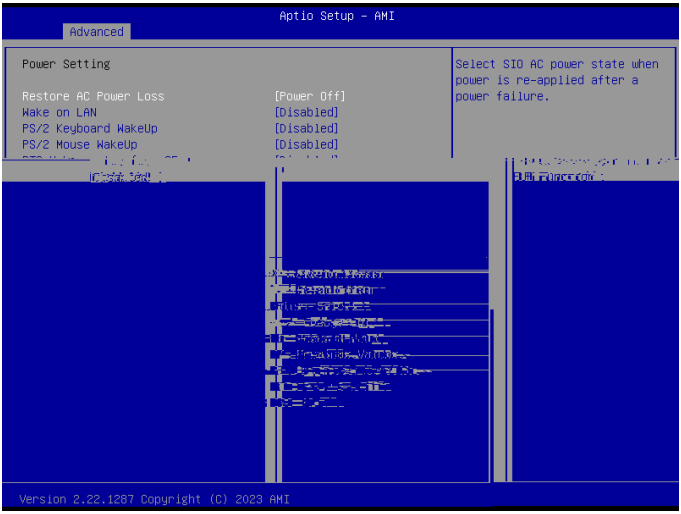
TPM Deyice Selecwion

TPM G

dTPM,PTT,Auto.

<Esc> d Advanced d

PozerManagemenw Con gxrawion <Enwer>



Revwore AC Pozer Lovv

G

Power Of ,Power On ,Last State.

Wake on LAN

G

Enabled,Disabled.

PS/2 Ke|board WakeUp

PS/2 G

Enabled,Disabled.

PS/2 Moxve WakeUp

PS/2 G

Enabled,Disabled.

RTC Wake v|vwem from S5

RTC G

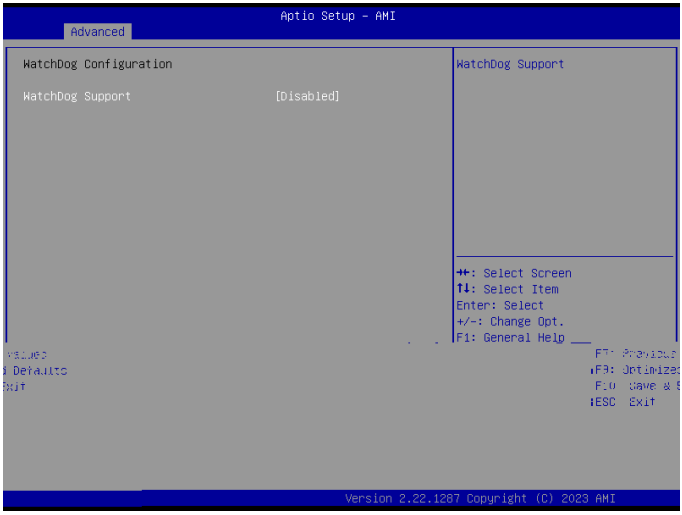
Disabled, Fixed Time,Dynamic Time.

EUP Fxncwion

G

Enabled,Disabled.

WaᴡchDog Con gxraᴡion <Enᴡer>



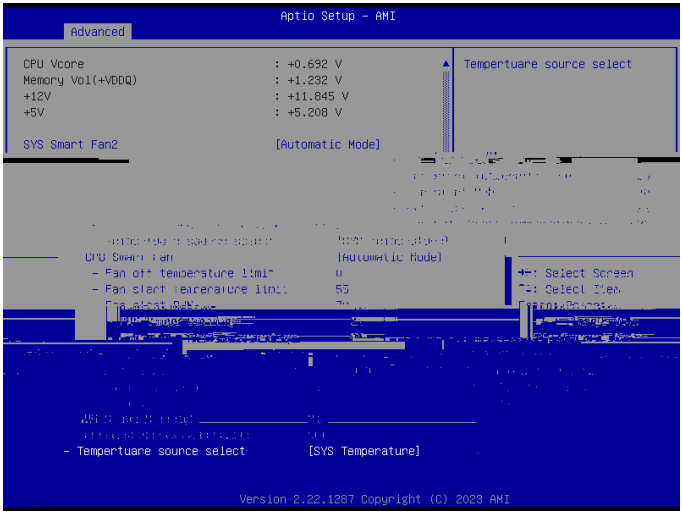
WaᴡchDog Sxpporᴡ

G

Enabled,Disabled.

<Esc> d Advanced d

Hard z are Moniᴡor <Enᴡer>



SYS Smarw Fan2/1

G

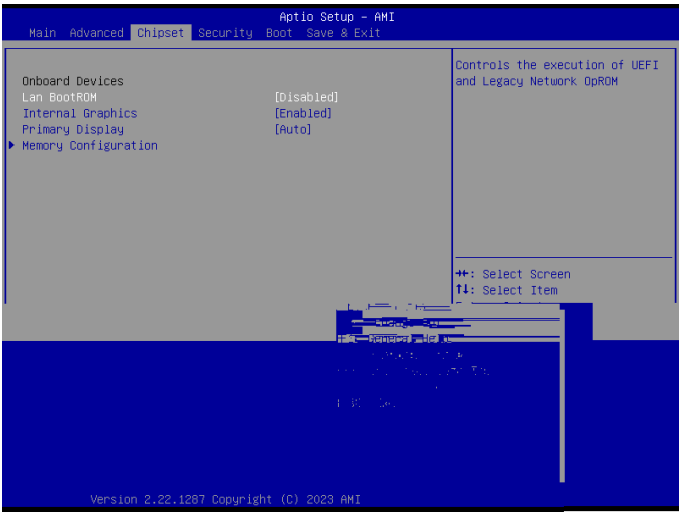
Full on Mode,Automatic Mode,Manual Mode.

Temperwxare voxrce velecw

s G

CPU Temp,SYS Temperature.

4.5 Chipvew



Lan BoowROM

G

Disable,Enable.

Inwernal Graphicv

G

Auto,Disable,Enable.

Primar| Divpla|

Hu G

Auto,IGFX,PEG Slot.

Memor| Con gxrawion

G

4.6 Secxriw|



Adminivwrawor Pavvzord

1. Administrator Password <Enter> G
2. dCreate New Password d 3~20

<Enter> dConfrm Password d G

dInvalid Password! d G

dAdministrator Password d dEnter Current Password d

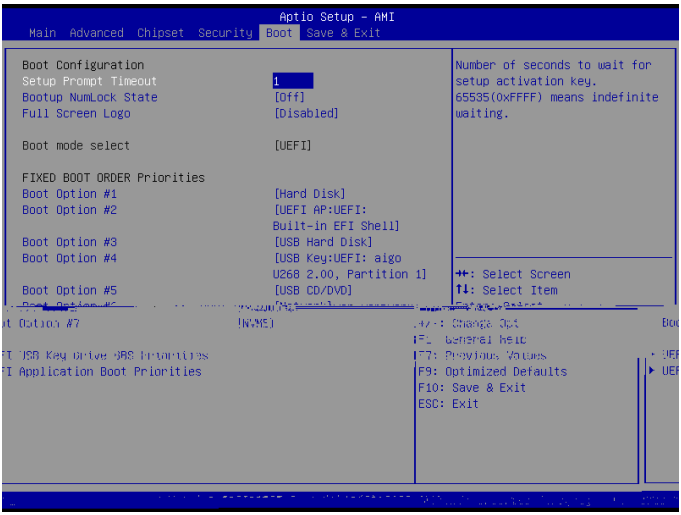
dCreate New Password d <Enter> G

Adminivwrawor Pavvzord

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Setup,Setup&Post.

4.7 Boow



Sewxp Prompw Timeoxw

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Boowxp NxmLock Swawe

Numlock G On NumLock

G Of Numlock G

On,Of.

FxII Screen Logo

G

Enabled,Disabled.

Boow Opwion #1/2/3/4/5/6/7

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NVMe,Hard Disk,CD/DVD,USB Hard Disk,USB Key,USB CD/DVD,Net work,UEFI AP.

4.8 Saye & E{iw

Saye Changev and Revew

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Divcard Changev and Revew

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Revwore Defaxlwv

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6 WDT

6.1

WDT SIO LDN DEV7, 0X72 BIT7 1 0
0XF3 0X72 BIT7 1 0XF3 0X20 32

6.1.1

```
//enter ite config
        IoWrite8(SioIndexPort,0x87);
        IoWrite8(SioIndexPort,0x01);
        IoWrite8(SioIndexPort,0x55);
        IoWrite8(SioIndexPort,0x55);

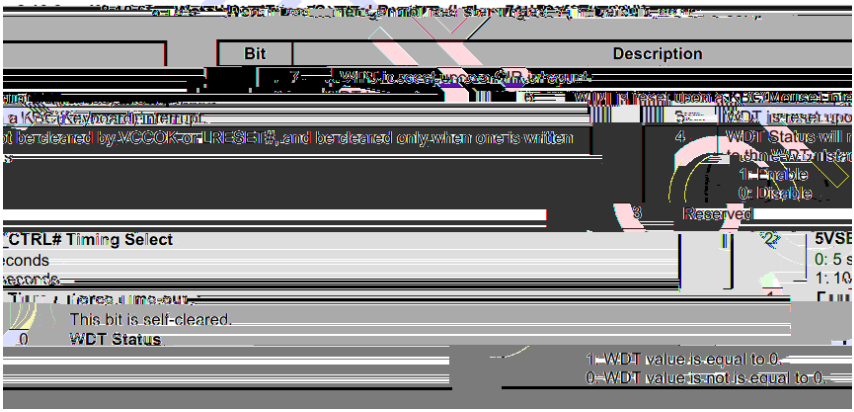
//select watchdog device
        IoWrite8(SioIndexPort,0x07);
        IoWrite8(SioDataPort,0x07);

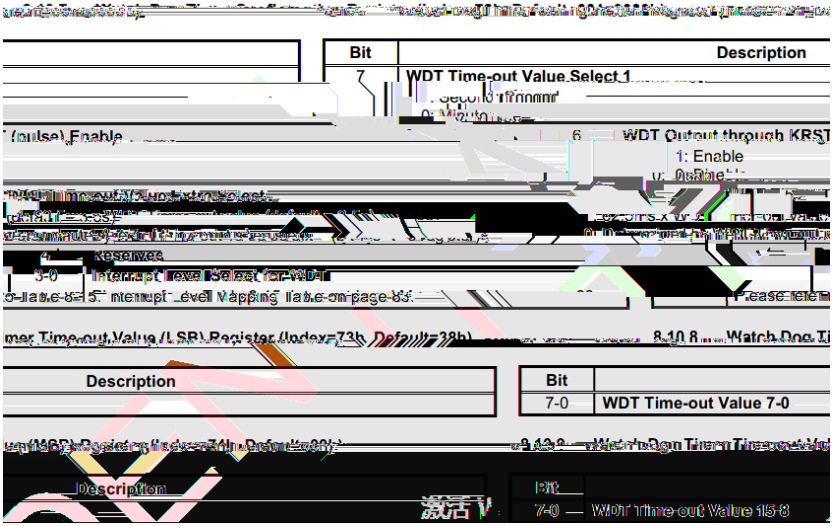
 IoWrite8(SioIndexPort, 0xF1);
 temp_data = IoRead8(SioDataPort);
 temp_data &= 0xBB;
 temp_data |= 0x44;
 IoWrite8(SioIndexPort, 0xF1);
 IoWrite8(SioDataPort, temp_data);
 //MAP to GP40
 IoWrite8(SioIndexPort, 0xF4);
 temp_data = IoRead8(SioDataPort);
 temp_data &= 0xC0;
 temp_data |= 0x20;
 IoWrite8(SioIndexPort, 0xF4);
IoWrite8(SioDataPort, temp_data);
 //wdt start
 if(ite_watch_dog_element->wdt_operator==START_WDT){

  //clear status
  IoWrite8(SioIndexPort, 0x71);
  temp_data = IoRead8(SioDataPort);
  temp_data &= 0xfe;
  IoWrite8(SioIndexPort, 0x71);
  IoWrite8(SioDataPort, temp_data);
```

6.1.2

```
//wdt stop
if(ite_watch_dog_element->wdt_operator==STOP_WDT){
  IoWrite8(SioIndexPort, 0x74);
  IoWrite8(SioDataPort, 0x0);
  IoWrite8(SioIndexPort, 0x73);
  IoWrite8(SioDataPort, 0x0);
                                                    }
```





7 GPIO

7.1 GPIO

PCH H610 GPIO GPP_F0,GPP_F1, ,GPP_F2,GPP_F3,GPP_F4,GPP_F5,GPP_F6,GPP_F7

0XE06A09E0 -- GPIO GPP_F0

0XE06A09F0 -- GPIO GPP_F1

0XE06A0A00 -- GPIO GPP_F2

0XE06A0A10 -- GPIO GPP_F3

0XE06A0A20 -- GPIO GPP_F4

0XE06A0A30 -- GPIO GPP_F5

0XE06A0A40 -- GPIO GPP_F6

0XE06A0A50 -- GPIO GPP_F7

Sample code AioT0_H610_ite_pch_gpio_c_v0.1 Handle_Pch_GPIO